

<b>PTO-1449</b>  <b>Information Disclosure Citation in an Application</b>		Application No. 09/617,669		Applicant(s) Eric P. Traut, Benjamin Martz	
		Docket Number 068167.0103		Group/Unit 2751	Filing Date July 17, 2000

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U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<u>u</u>	A	5278973	1/11/94	O'Brien et al.	395	500	6/27/91
<u>u</u>	B	5301277	4/5/94	Kanai	709	301	12/23/91
<u>u</u>	C	5367628	11/22/94	Ote et al.	345	501	10/15/92
<u>u</u>	D	5448264	9/5/95	Pinedo et al.	345	508	6/14/93
<u>u</u>	E	5452456	9/19/95	Mourey et al.	713	100	12/18/92
<u>u</u>	F	5502809	3/26/96	Takano	345	509	9/6/94
<u>u</u>	G	5640562	6/17/97	Wold et al.	395	652	2/27/95
<u>u</u>	H	5666521	9/9/97	Marisetty	345	525	11/20/96
<u>u</u>	I	5742797	4/21/98	Celi, Jr. et al.	345	507	8/11/95
<u>u</u>	J	5752275	5/12/98	Hammond	711	207	7/14/97
<u>u</u>	K	5757386	5/26/98	Celi, Jr. et al.	345	507	8/11/95
<u>u</u>	L	5790825	8/4/98	Traut	712	209	8/5/97
<u>u</u>	M	5831607	11/3/98	Brooks	345	333	1/25/96
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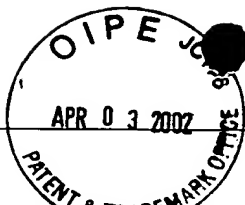
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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO

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		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	
<u>u</u>	S	"Processor Instruction Sets," The PC Guide, version date 12/18/00, <a href="http://www.pcguide.com/ref/cpu/arch/int/inst-c.html">http://www.pcguide.com/ref/cpu/arch/int/inst-c.html</a> .	
<u>u</u>	T	"M68060 User's Manual," Motorola, 1994, pp. i-xviii; Section 4, Memory Management Unit, pp. 4-1 to 4-30, <a href="http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf">http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf</a> .	
<u>u</u>	U	"MPC750, RISC Microprocessor User's Manual," Motorola, 8/97, Contents, pp. iii-xvi; Chapter 5, Memory Management, pp. 5-1 to 5-34; Glossary, pp. Glossary-1 to Glossary-13, <a href="http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf">http://e-www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf</a> .	

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	Docket Number 068167.0103	Group Art Unit 2751	Filing Date July 17, 2000

**Information Disclosure Citation  
in an Application**

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		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<u>u</u>	A	5,815,686	9/29/98	Earl, et al.	395	500	9/12/96
<u>u</u>	B	4,779,188	10/18/88	Gum, et al.	364	200	10/19/87

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							YES	NO
<u>u</u>	C	EP 0 645 701 A2	26/09/94	EPO	G06F	9/455	X	

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		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	
<u>u</u>	D	PCT International Search Report in International Application No. PCT/US 01/22276, International filing date 16/07/01, mail date 07/03/02.	
<u>u</u>	E	Traut E, "Building the Virtual PC," Byte, McGraw-Hill Inc., Vol. 22, No. 11, pp. 51-52, 1 November 1997.	
<u>u</u>	F	"Intel386 DX Microprocessor," Intel, pp. 32-58, 31 December 1995.	
<u>u</u>	G	"Macintosh and Technology: Changing Chips in the Middle of the Stream, or Apple Takes a Risc," URL:www.btech.co/changingchips.html, paragraphs '0006!'-'0007!', retrieved 12/10/01.	
<u>u</u>	H	"M68040 User's Manual," Motorola, Inc., Chapter 3, copyright 1990, revised 1992, 1993.	
<u>u</u>	I	Osisek DL et al., "ESA/390 Interpretive-Execution Architecture, Foundation for VM/ESA," IBM Systems Journal, Vol. 30, No. 1, pp. 34-51, 1991.	
<u>u</u>	J	Shang Rong Tsai et al., "On the Architectural Support for Logical Machine Systems," Microprocessing and Microprogramming, Vol. 22, No. 2, pp. 81-96, February 1988.	

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PTO-1429 <b>Information Disclosure Citation in an Application</b>	Application No. 09/617,669	Applicant(s) Eric P. Traut, Benjamin Martz	
	Docket Number 068167.0103	Group Art Unit 2751	Filing Date July 17, 2000

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u	B	5,699,539	12/16/1997	Garber et al.	395	402	05/07/1996
u	C	5,617,552	04/01/1997	Garber et al.	395	401	02/29/1996
u	D	5,675,382	10/07/1997	Bauchspies	348	390	04/08/1996
u	E	5,768,593	06/16/1998	Walters et al.	395	705	03/22/1996
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u	G	6,115,054	09/05/2000	Giles	345	522	12/29/1998

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